FIG 1

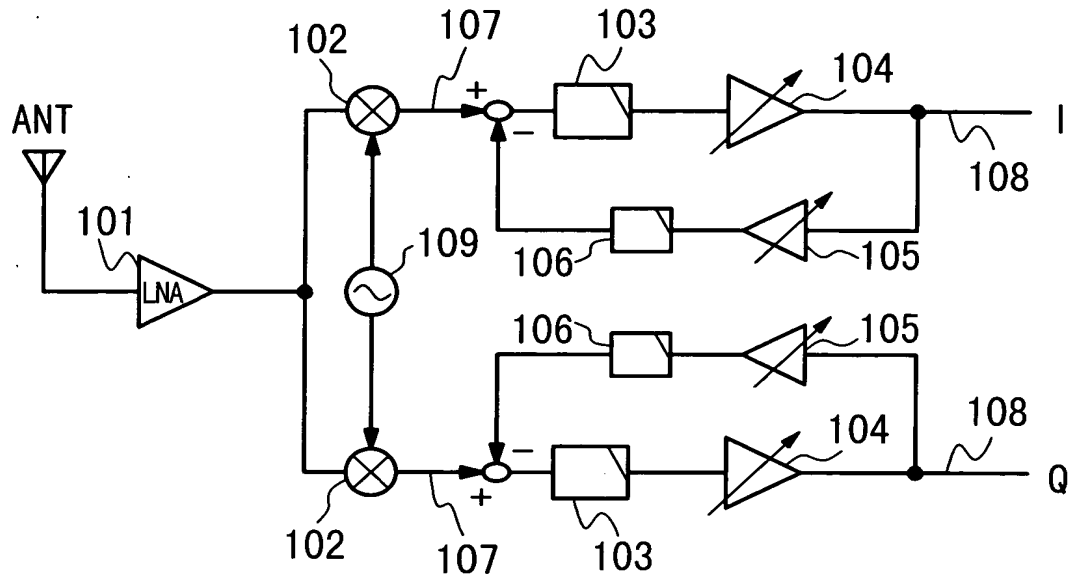

ANT
101
LNA
102
107
103
104
105
106
108
109
I
Q
+
−

FIG. 2

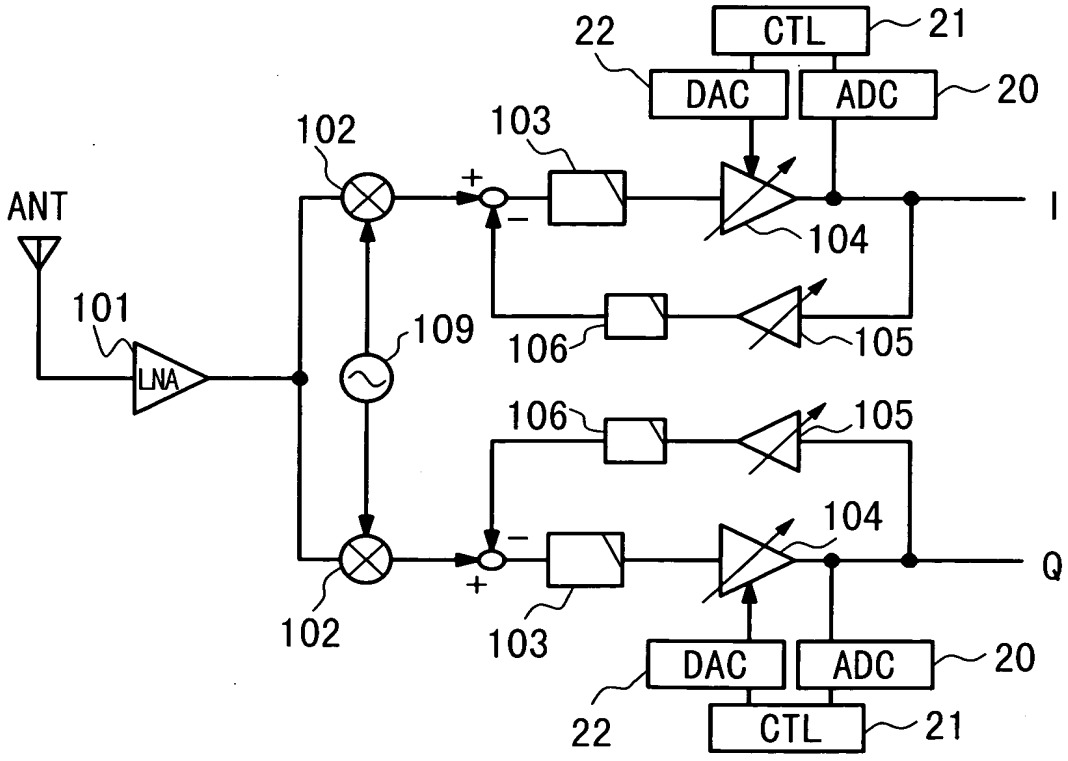

ANT
101
LNA
102
103
104
105
106
107
108
109
CTL
DAC
ADC
20
21
22
I
Q
+
−

FIG. 3

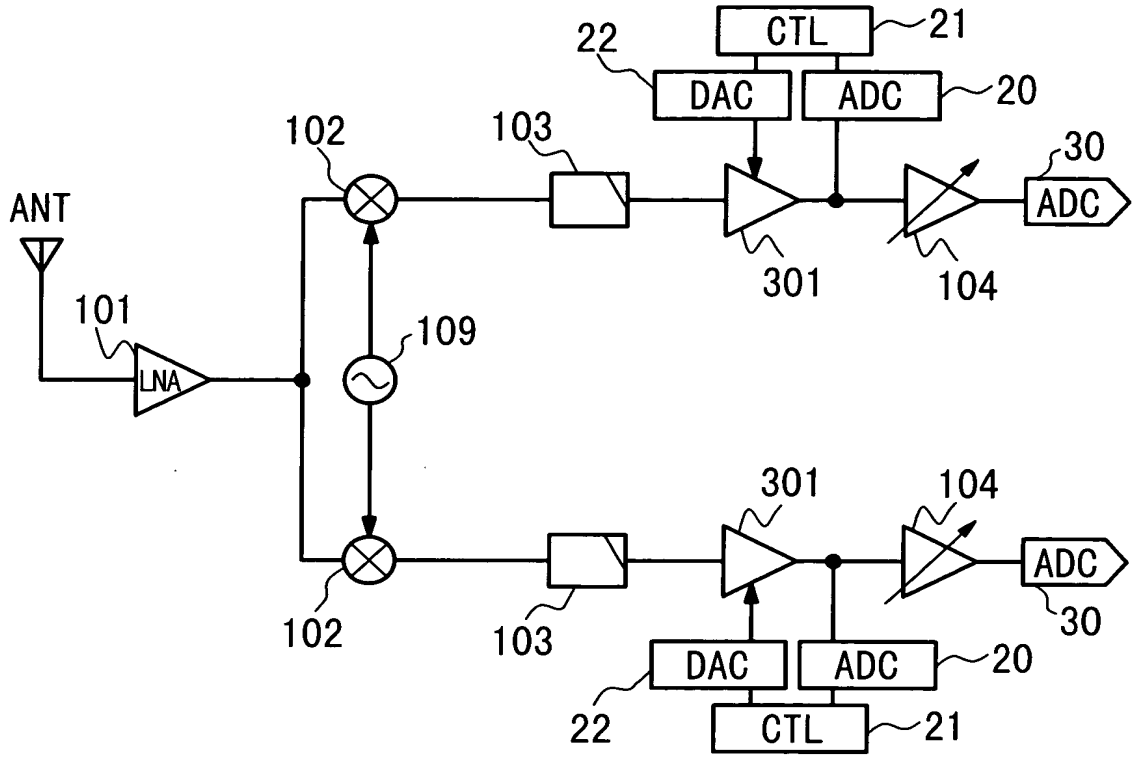

CTL
21
22
DAC
ADC
20
102
103
30
ADC
ANT
301
104
101
109
LNA
301
104
ADC
30
102
103
DAC
ADC
20
22
CTL
21

FIG. 4

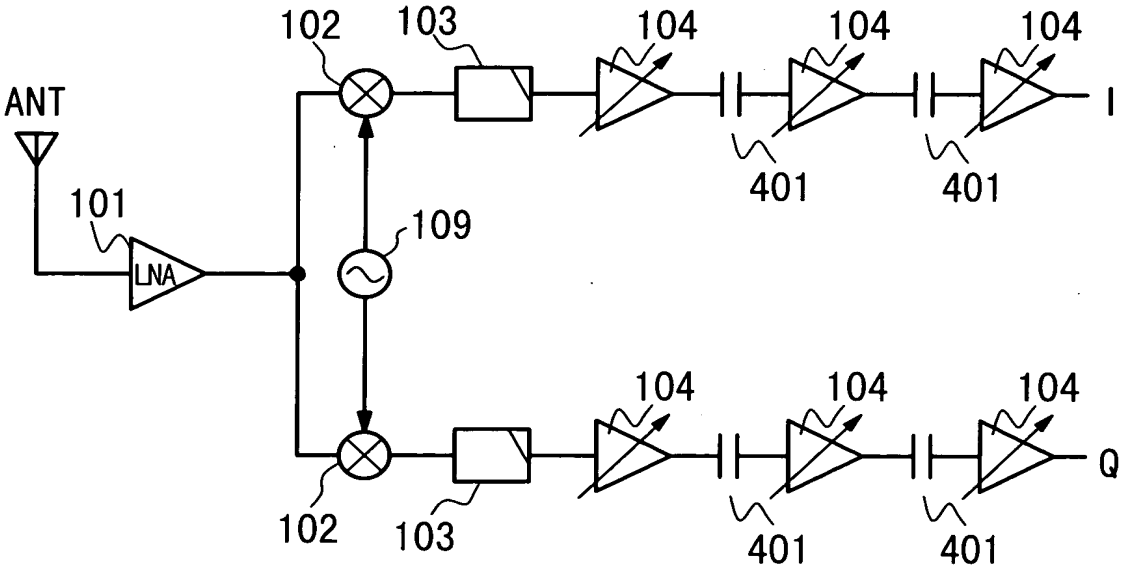

102
103
104
104
104
I
ANT
401
401
101
109
LNA
104
104
104
Q
102
103
401
401

FIG. 5

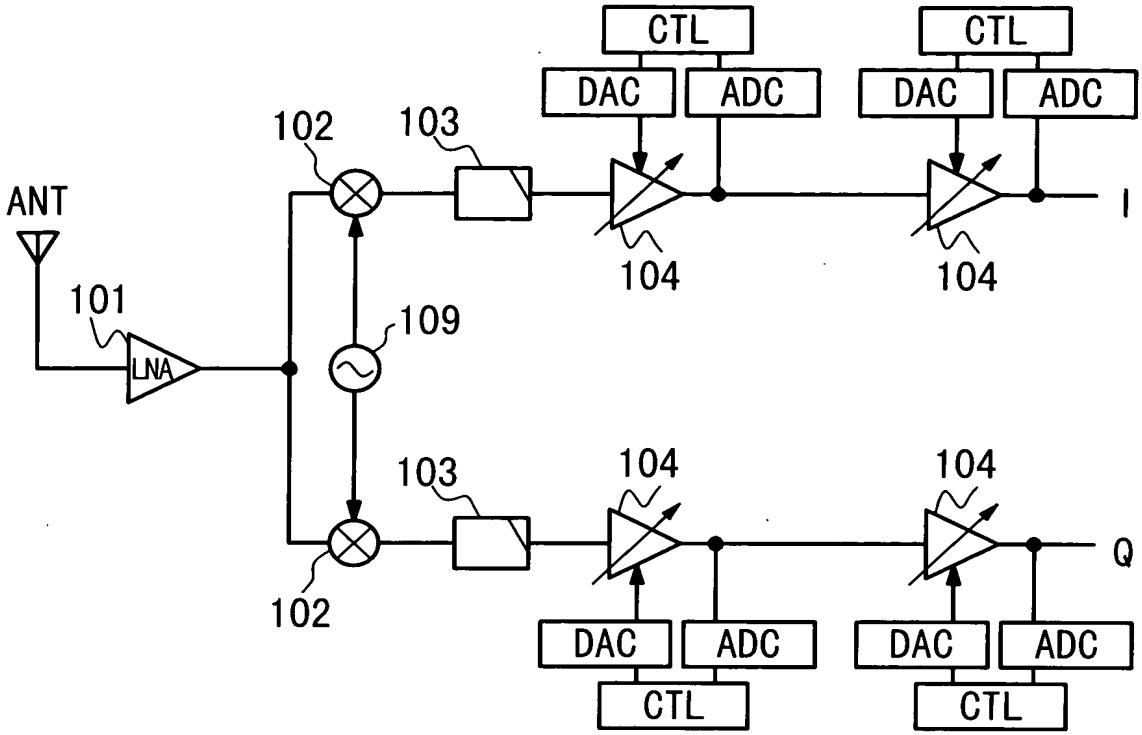

CTL
DAC
ADC
CTL
DAC
ADC
102
103
ANT
I
104
104
101
109
LNA
103
104
104
Q
102
DAC
ADC
DAC
ADC
CTL
CTL

FIG. 6

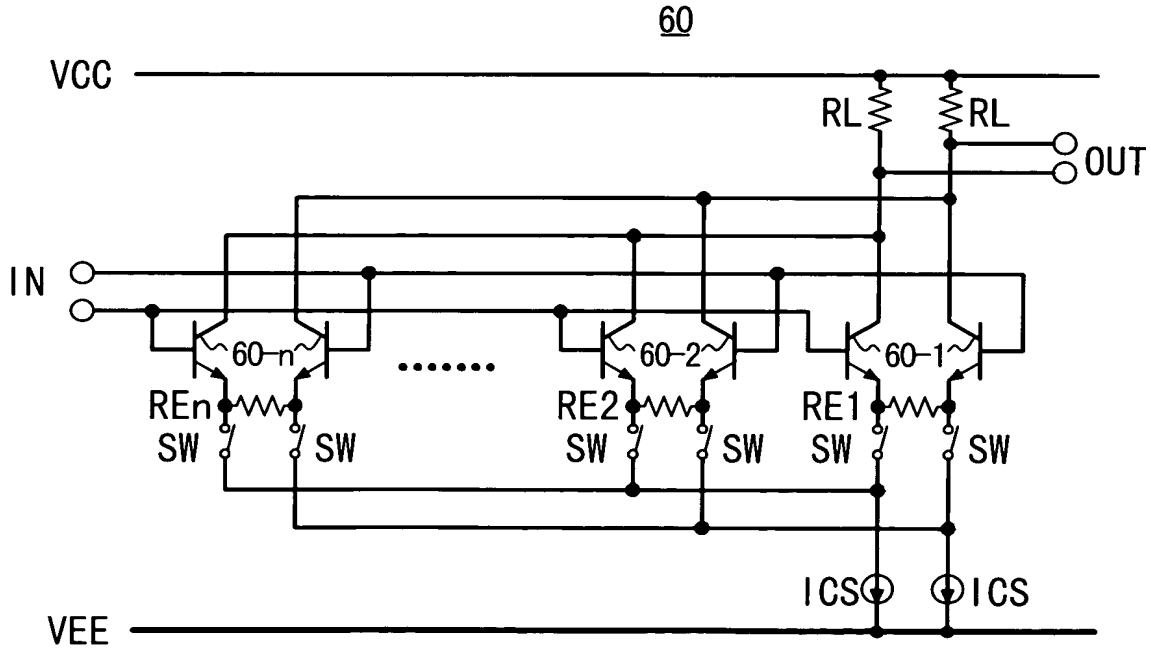

60
VCC
RL
RL
OUT
IN
60-n
60-2
60-1
REn
RE2
RE1
SW
SW
SW
SW
SW
SW
ICS
ICS
VEE

FIG. 7

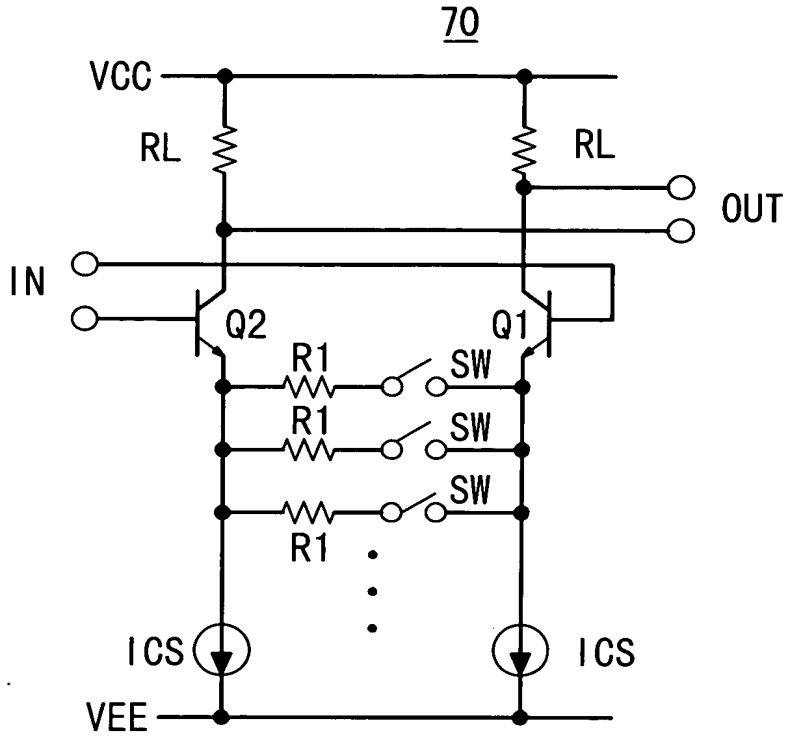

70
VCC
RL
RL
OUT
IN
Q2
Q1
R1
SW
R1
SW
SW
R1
ICS
ICS
VEE

FIG. 10

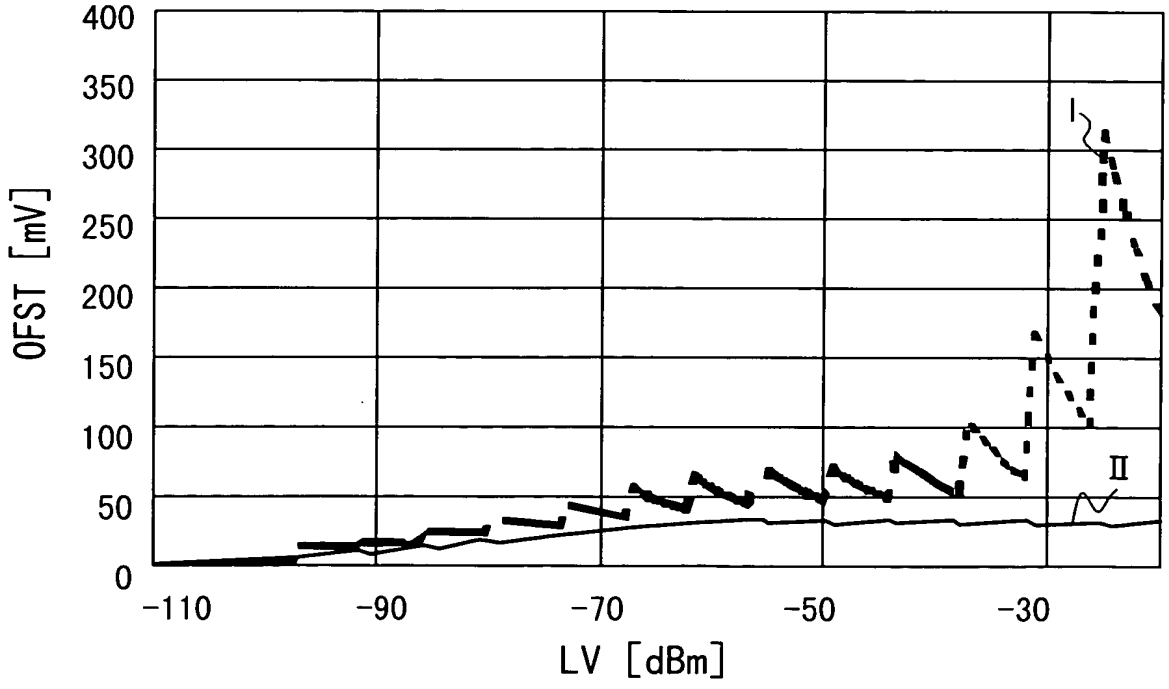

400
350
300
250
200
150
100
50
0
OFST [mV]
-110
-90
-70
-50
-30
LV [dBm]
I
II

FIG 8

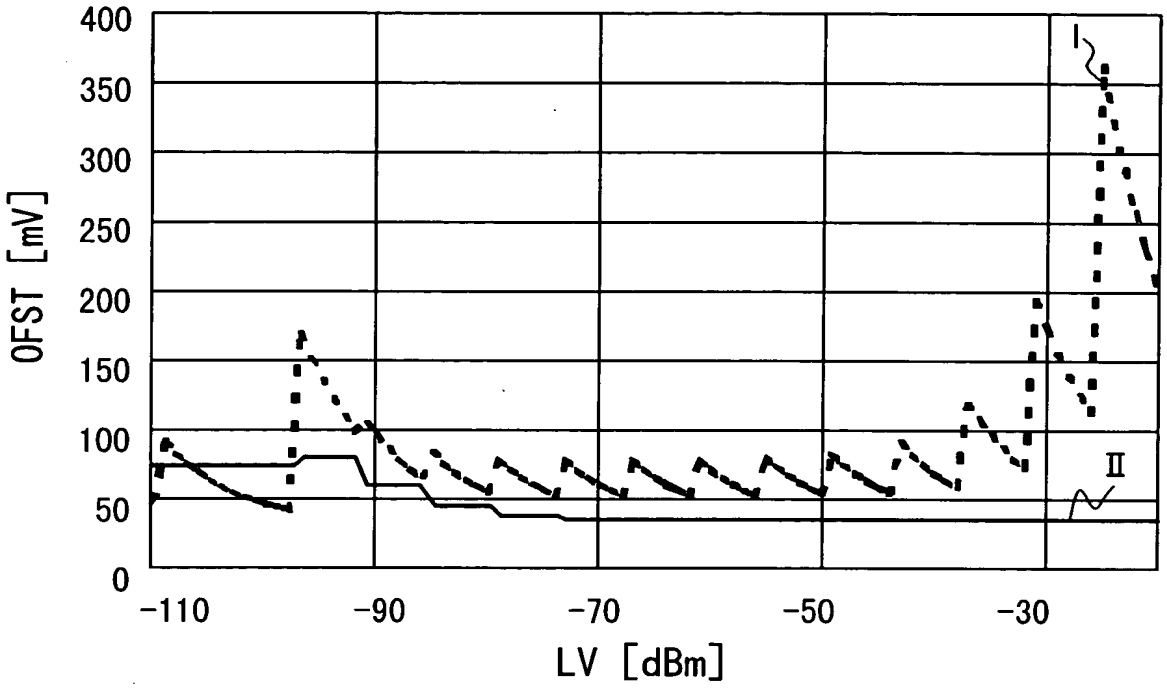

400
350
300
250
200
150
100
50
0
OFST [mV]
-110
-90
-70
-50
-30
LV [dBm]
I
II

FIG. 9

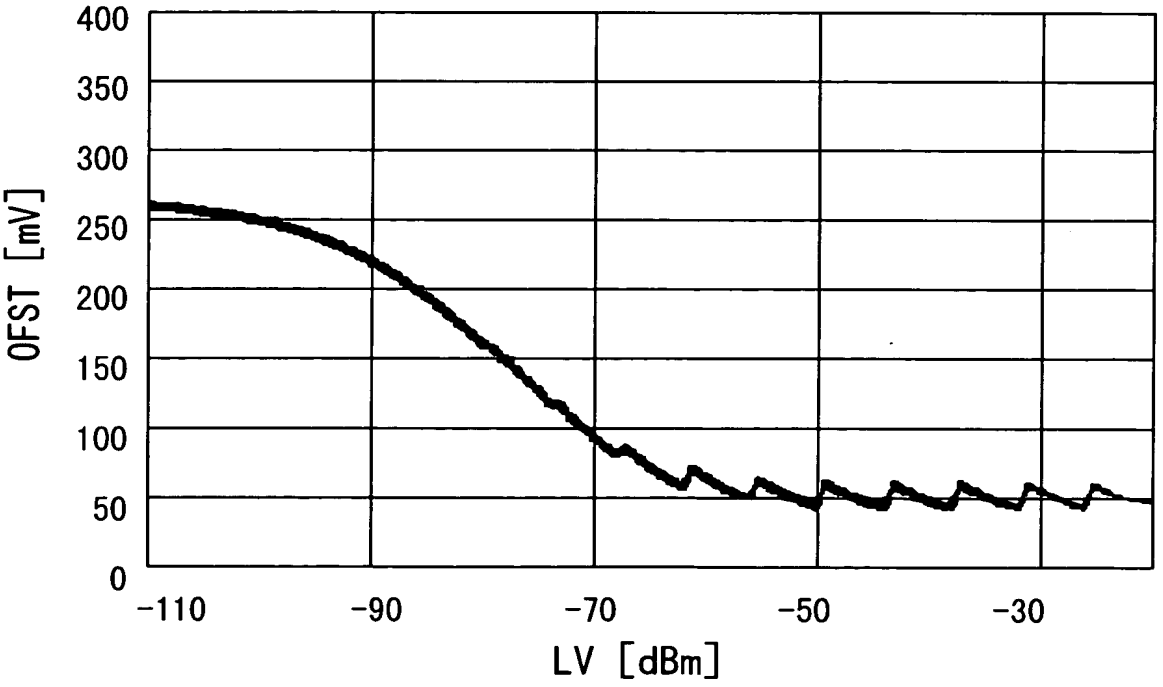

400
350
300
250
200
150
100
50
0
OFST [mV]
-110
-90
-70
-50
-30
LV [dBm]

FIG. 11

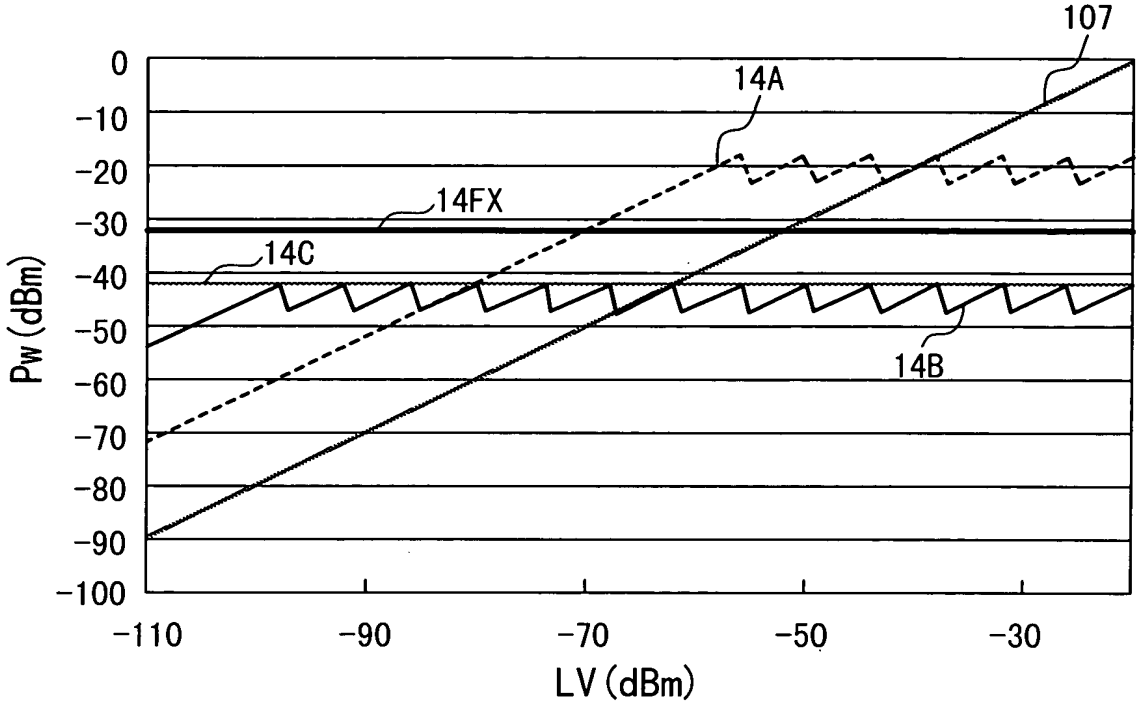
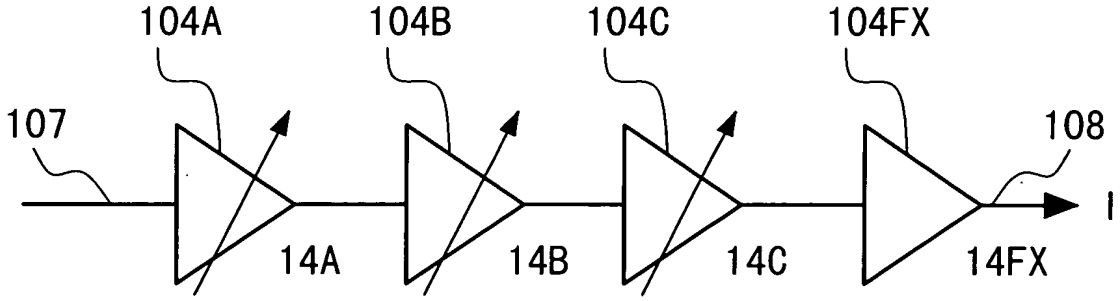

107
14A
14FX
14C
14B
0
-10
-20
-30
-40
-50
-60
-70
-80
-90
-100
Pw (dBm)
-110
-90
-70
-50
-30
LV (dBm)

FIG. 12


104A
104B
104C
104FX
107
108
I
14A
14B
14C
14FX